

1. Filtering and interference suppression device (62), particularly of the broad band type, for an electric motor (34) comprising at least a first powering brush (16) for an armature commutator of the electric motor (34), of the type comprising a capacitor (64), one terminal of which is electrically connected to a strip conductor (38) that electrically powers the first brush (16) powering the armature commutator of the electric motor (34), and the other terminal of which is electrically connected to a ground strip conductor (58), connected, in turn, to the electrical ground (60) of the electric motor (34), characterized in that the capacitor (72) of the filtering and interference suppression device (62) is of the non-inductive type, and in that each of the non-inductive capacitors (72) is directly attached to a circuit board (73) comprising strip conductors, of which are at least one powering strip conductor (38, 40) for a brush and one ground strip conductor (58).

2. Filtering and interference suppression device (62) for an electric motor (34) according to the preceding claim, characterized in that it comprises at least a first (16) and second powering brush (18) for the armature commutator, each of which is connected to the electrical ground (60) of the motor (34), with interposition of a capacitor (72), one terminal (74, 76) of which is connected to a strip conductor (38, 40) electrically powering the corresponding brush (16, 18), and the other ground terminal (78, 80) of which is connected to the ground (60), and in that each capacitor (72) is a capacitor (72) of the non-inductive type.

3. Filtering and interference suppression device (62) according to the preceding claim, characterized in that the two filtering and interference suppressing capacitors (72) of the non-inductive type are made in the form of a double non-inductive capacitor (72).

4. Filtering and interference suppression device (62) according to any of the preceding claims, characterized in that the circuit board (73) is the brush-bearing board (10) for the electric motor (34).

5. Filtering and interference suppression device (62) according to any of the preceding claims, characterized in that each non-inductive filtering and interference suppressing capacitor

(72) is of the CMS type.

6. Filtering and interference suppression device (62) according to any of the preceding claims, characterized in that the electric terminals (74, 76, 78, 80) of each non-inductive capacitor (72) are electrically connected and directly attached to the corresponding strip conductors (38, 40, 58).

7. Filtering and interference suppression device (62) according to the preceding claim, characterized in that each terminal (74, 76, 78, 80) is attached and electrically connected to the corresponding strip by soldering.

8. Filtering and interference suppression device (62) according to claim 6 or 7, characterized in that each terminal (74, 76, 78, 80) is attached and electrically connected to the corresponding strip (38, 40, 58) by gluing with a conductive glue.

9. Filtering and interference suppression device (62) according to any of the preceding claims, characterized in that it comprises at least one other capacitor (82, 84) interposed between the ground strip (58) and one section of the power strip (38, 40) for at least one of the brushes (16, 18) powering the armature commutator of the electric motor (34) which is located between the non-inductive capacitor (72) and the corresponding brush (16, 18).

10. Filtering and interference suppression device (62) according to the preceding claim, characterized in that at least one of the other capacitors (82, 84) is of the CMS type.

11. Filtering and interference suppression device (62) according to any of the preceding claims, characterized in that the section of at least one of the power strips (38, 40) for one of the brushes (16, 18) powering the commutator, which is located between the non-inductive capacitor (72) and the corresponding brush (16, 18), comprises a choke (86, 88) connected in series.

12. Filtering and interference suppression device (62) according to any of the preceding claims, characterized in that it comprises a choke connected in series on the ground strip (58) between one ground terminal (78, 80) of the non-inductive capacitor (72) and the brush (20).

13. Filtering and interference suppression device (62) according to claim 11 or 12, characterized in that at least one of the chokes (86, 88) is of the high-frequency type.

14. Filtering and interference suppression device (62) according to any of claims 11 to 13, characterized in that at least one of the chokes (86, 88) is coiled, and in that it comprises at least one space that separates two juxtaposed turns.

15. Filtering and interference suppression device (62) according to any of claims 11 to 14, characterized in that at least one of the chokes (86, 88) is of the CMS type.

16. Filtering and interference suppression device (62) according to any of the preceding claims, characterized in that it comprises thermal protector (90) located on the ground strip (58).

17. Filtering and interference suppression device (62) according to any of claims 9 to 16, characterized in that it comprises at least one peak-limiter connected in parallel with the non-inductive capacitor (72), between the ground strip (58) and one of the strip conductors (38, 40).

18. Filtering and interference suppression device (62) according to the preceding claim, characterized in that at least one of the peak-limiters is of the CMS type.